

ABSTRACT

A liquid crystal display (LCD) including a first substrate and a second substrate. A plurality of gate lines and data lines are formed on the first substrate perpendicular to each other to define a plurality of pixel regions. A thin film transistor is formed for every one pixel. A common electrode is formed on the second substrate. A gate pad, a data pad, and a common electrode pad electrically are connected to each of the gate lines, data lines, and common electrodes respectively. A data on/off pad is formed between adjacent data pads in substantially the same pattern as the data pad for testing a data signal applied to the pixel region. A gate on/off pad is formed between adjacent gate pads in substantially the same pattern as the gate pad for testing a gate signal applied to the pixel region. A common electrode on/off pad is provided for testing a common electrode signal applied to the pixel region. The LCD thereby prevents damage from a rubbing cloth caused by the spacing between the on/off pads, to improve a picture quality.